

Notice of References Cited	Application/Control No. 10/083,931	Applicant(s)/Patent Under Reexamination CHANDRAN ET AL.	
	Examiner Nitin C. Patel	Art Unit 2116	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,337,833 B1	01-2002	Kanazashi et al.	365/233
	B	US-2003/0154365 A1	08-2003	Chandran et al.	712/218
	C	US-4,511,993	04-1985	Bromme et al.	713/600
	D	US-5,563,835 A	10-1996	Oldham, Harry E.	365/207
	E	US-6,138,211 A	10-2000	Ahn et al.	711/136
	F	US-4,638,459	01-1987	Pechar et al.	365/104
	G	US-6,594,185 B1	07-2003	Takagi, Tetsuo	365/195
	H	US-6,339,552 B1	01-2002	Taruishi et al.	365/189.05
	I	US-5,623,615 A	04-1997	Salem et al.	712/238
	J	US-6,687,843 B2	02-2004	Kwak, Jong Tae	713/500
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Steven Wallace et al.; Title: "Design and Implementation of a 100 Mhz Reorder Buffer" IEEE 1995, -07803-2428-5/95
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.